CHI MEI
OPTOELECTRONICS CORP.

TFT LCD Preliminary Specification

MODEL NO.: V201V2 - T01

LCD TV Head Division	
Director	

QRA Dept.	TD Division	DDII	DDI
Approval	Approval	Approval	Approval

LCD TV Marketing and Product Management Dept.	
Project Manager	

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REVISION HISTORY

Version	Date	Page (New)	Section	Description
Ver 1.0	Dec.15,'03	All	All	Preliminary Specification was first issued.

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1. GENERAL DESCRIPTION

1.1 OVERVIEW

V201V2-T01 is a 20.1" TFT Liquid Crystal Display module with 12-CCFL Backlight unit and 1ch-TTL interface. This module supports 640 x 480 VGA format and can display true 16.7M colors (8-bit/color).

1.2 FEATURES

- High brightness (500 nits)

- High contrast ratio (600:1)

- Fast response time

- High color saturation NTSC 75%

- VGA (640 x 480 pixels) resolution

- DE (Data Enable) only mode

- TTL interface

1.3 APPLICATION

- TFT LCD TVs

1.4 GENERAL SPECIFICATI0NS

Item	Specification	Unit	Note
Active Area	408 (H) x 306 (V) (20.1" diagonal)	mm	(1)
Bezel Opening Area	412 (H) x 310 (V)	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	640 x R.G.B. x 480	pixel	-
Pixel Pitch(Sub Pixel)	0.2125 (H) x 0.6375 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	16.7M	color	-
Display Operation Mode	Transmissive mode / Normally black	-	-
Surface Treatment	Hardness : 3H, Haze : 40% Anti-reflective coating < 2% reflection	-	-

1.5 MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal(H)	435.7	436.0	436.4	mm	(1)
	Vertical(V)	329.0	329.3	329.7	mm	
	Depth(D)	-	40.38	41.38	mm	with INV Cover
Weight		-	2200	2300	g	

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

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2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	T_{ST}	-20	+60	ºC	(1)
Operating Ambient Temperature	T_{OP}	0	50	ºC	(1), (2)
Shock (Non-Operating)	S_{NOP}	-	50	G	(3), (5)
Vibration (Non-Operating)	V_{NOP}	-	1.0	G	(4), (5)

Note (1) Temperature and relative humidity range is shown in the figure below.

   (a) 90 %RH Max. (Ta $\leqq$ 40 ºC).

   (b) Wet-bulb temperature should be 39 ºC Max. (Ta > 40 ºC).

   (c) No condensation.

Note (2) The temperature of panel display area surface should be 0 ºC Min. and 60 ºC Max.

Note (3) 11 ms, half sine wave, 1 time for ± X, ± Y, ± Z.

Note (4) 10 ~ 500 Hz, 10 min, 1 time each X, Y, Z.

Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

Relative Humidity (%RH)

Temperature (ºC)

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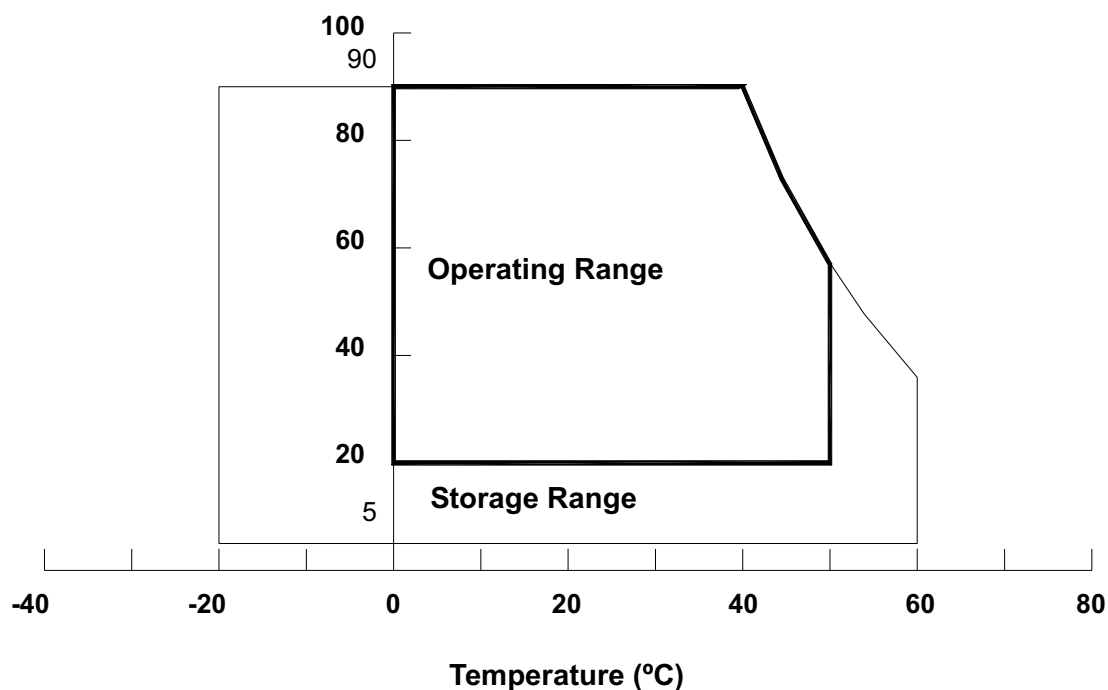
2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	Vcc	(-0.3)	(+5.5)	V	

2.2.2 BACKLIGHT INVERTER UNIT

Item	Symbol	Test Condition	Min.	Type	Max.	Unit	Note
Lamp Voltage	V_L	-	0	—	3.0K	V_{RMS}	(1), (2), $I_L = 4.2$ mA
Input Voltage	V_{BL}	—	0	—	30	V	
On/Off Control Voltage	VBLON	-	-0.3	-	7	V	
Internal/External PWM Select Voltage	VSEL	-					
Internal PWM Control Voltage	VIPWM	-					
External PWM Control Voltage	VEPWM	-					
Operating Temperature	Top	5～95%RH	0	—	75	℃	(3)
Storage Temperature	T_{ST}	5～95%RH	-30	—	80	℃	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation

should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to 3.2 for further information).

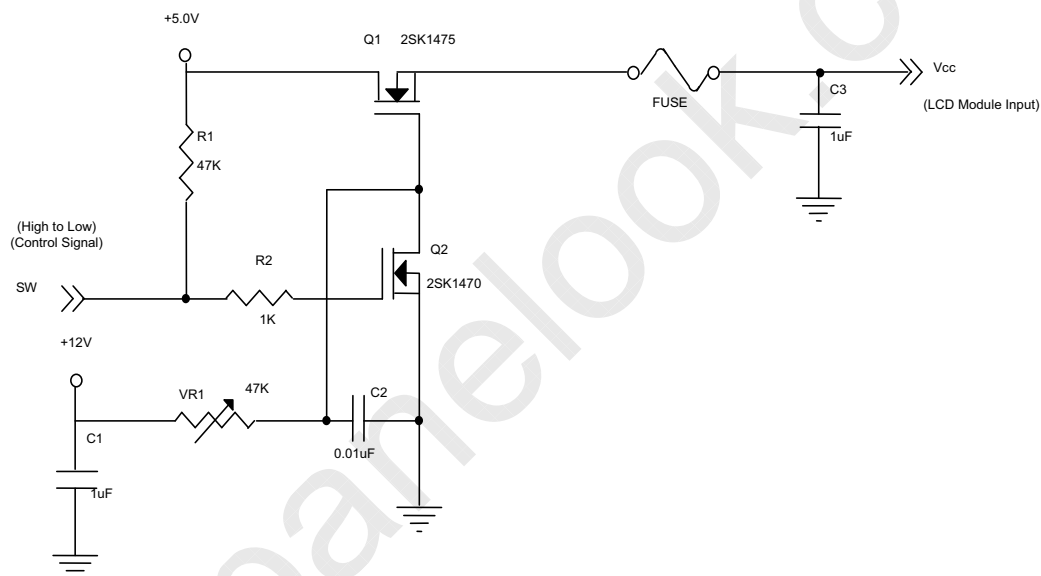Note (3) Protect inverters from moisture condensation and freezing.

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3. ELECTRICAL CHARACTERISTICS

3.1 TFT LCD MODULE

Ta = 25 ± 2 ºC

Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Power Supply Voltage		Vcc	4.5	5.0	5.5	V	(1)
Ripple Voltage		V_{RP}	-	150	-	mV	(2)
Rush Current		I_{RUSH}	-	1.9	2.5	A	
Power Supply Current	White	Icc	-	0.5	0.6	A	(3)
	Black		-	0.4	-	A	
	Vertical Stripe		-	0.4	0.5	A	
TTL input high threshold voltage		V_{IH}	(2.7)	-	(3.6)	V	
TTL input low threshold voltage		V_{IL}	0	-	(0.7)	V	

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:

Vcc rising time is 470μs

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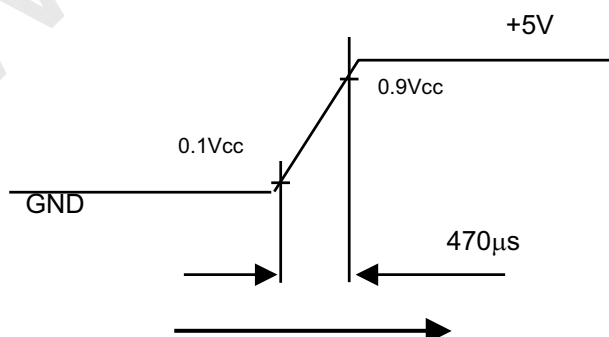
Note (3) The specified power supply current is under the conditions at Vcc = 5 V, Ta = 25 ± 2 ºC, f_v = 60 Hz, whereas a power dissipation check pattern below is displayed.

a. White Pattern
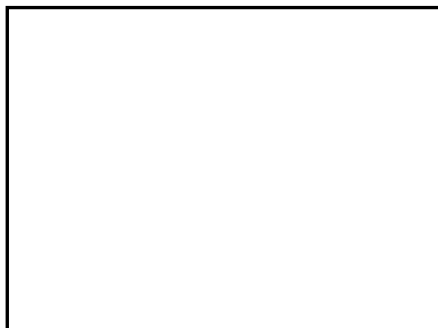
Active Area

b. Black Pattern

Active Area

c. Vertical Stripe Pattern

Active Area

R G B R G B
B R G B R G B R
B R G B R G B R
R G B R G B
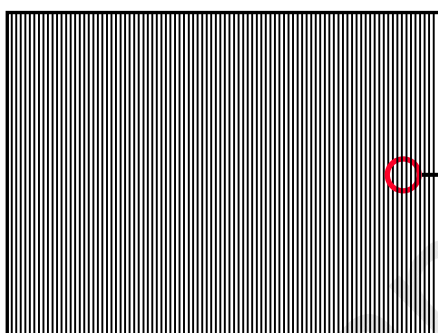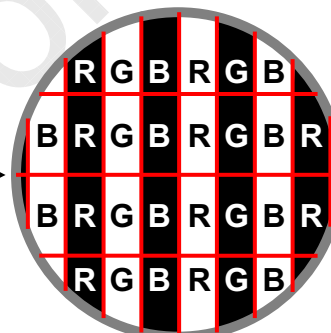
3.2 BACKLIGHT UNIT

Ta = 25 ± 2 ºC

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Lamp Input Voltage	V_L	733	815	896	V_{RMS}	I_L = 4.2mA
Lamp Current	I_L	3.9	4.2	4.5	mA_{RMS}	
Lamp Turn On Voltage	V_S	1650	-	3000	V_{RMS}	Ta = 0 ºC
		1320	-	3000	V_{RMS}	Ta = 25 ºC
Operating Frequency	F_L	31	33	35	KHz	
Lamp Life Time	L_{BL}	50K	60K	-	Hrs	
Power Consumption	P_L	-	51	-	W	Inverter Input

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Note (1) Lamp current is measured by utilizing high frequency current meters as shown below:
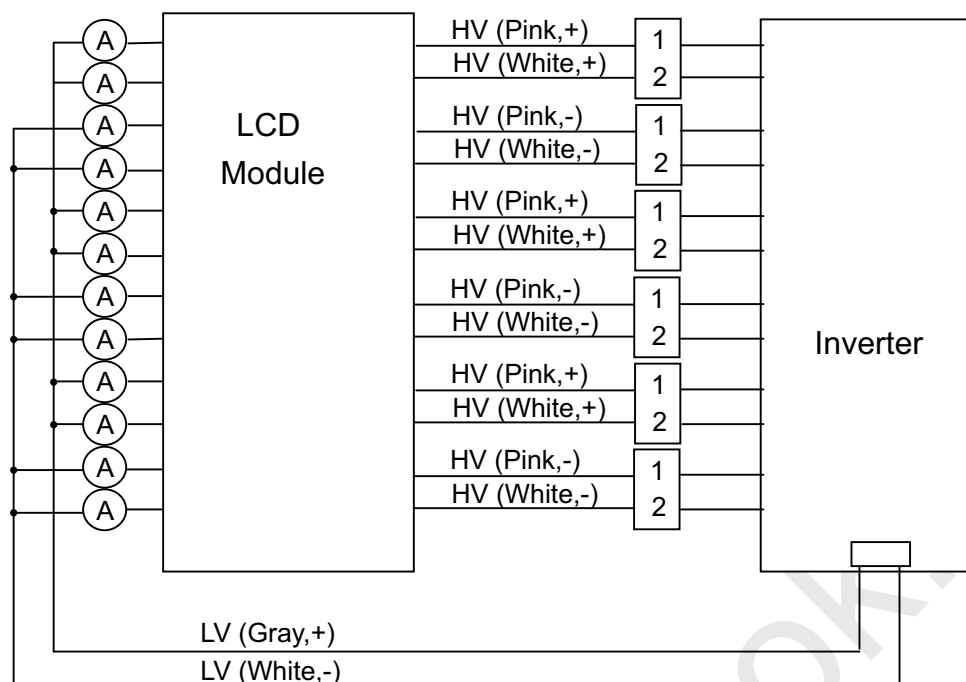
Note (2) The voltage shown above should be applied to the lamp for more than 1 second after startup. Otherwise the lamp may not be turned on.

Note (3) The lamp frequency may produce interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.

Note (4) $P_L = (\sum_{lamp1-lamp6} I_L \times V_L)/0.8$, P_L is based on the inverter efficiency, which is 80%.

Note (5) The lifetime of a lamp is defined as the time in which it continues to operate under the condition Ta = $25 \pm 2\ ^{\circ}C$ and I_L = 4.2 mArms until one of the following events occurs:

(a) When the brightness becomes equal or less than 50% of its original value.

(b) When the effective discharge length becomes equal or less than 80% of its original value. (Effective discharge length is defined as an area that has equal or more than 70% brightness compared to the brightness at the center point.)

Note (6) The waveform of the voltage output of inverter must be area-symmetric and the design of the inverter must have specifications for the modularized lamp. The performance of the Backlight, such as lifetime or brightness, is greatly influenced by the characteristics of the DC-AC inverter for the lamp. All the parameters of an inverter should be carefully designed to avoid producing too much current leakage from high voltage output of the inverter. When designing or ordering the inverter please make sure that a poor lighting caused by the mismatch of the Backlight and the inverter (miss-lighting, flicker, etc.) never occurs. If the above situation is confirmed, the module should be operated in the same manners when it is installed in your instrument.

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4. BLOCK DIAGRAM

4.1 TFT LCD MODULE

4.2 BACKLIGHT UNIT

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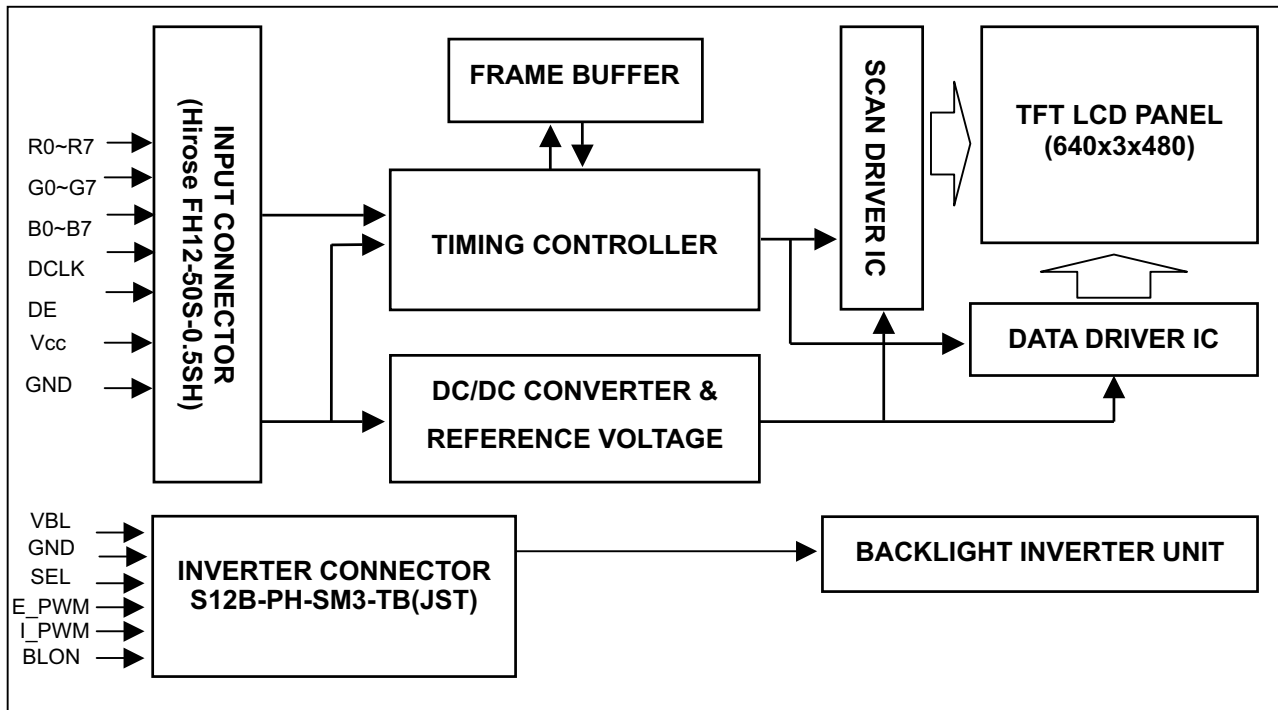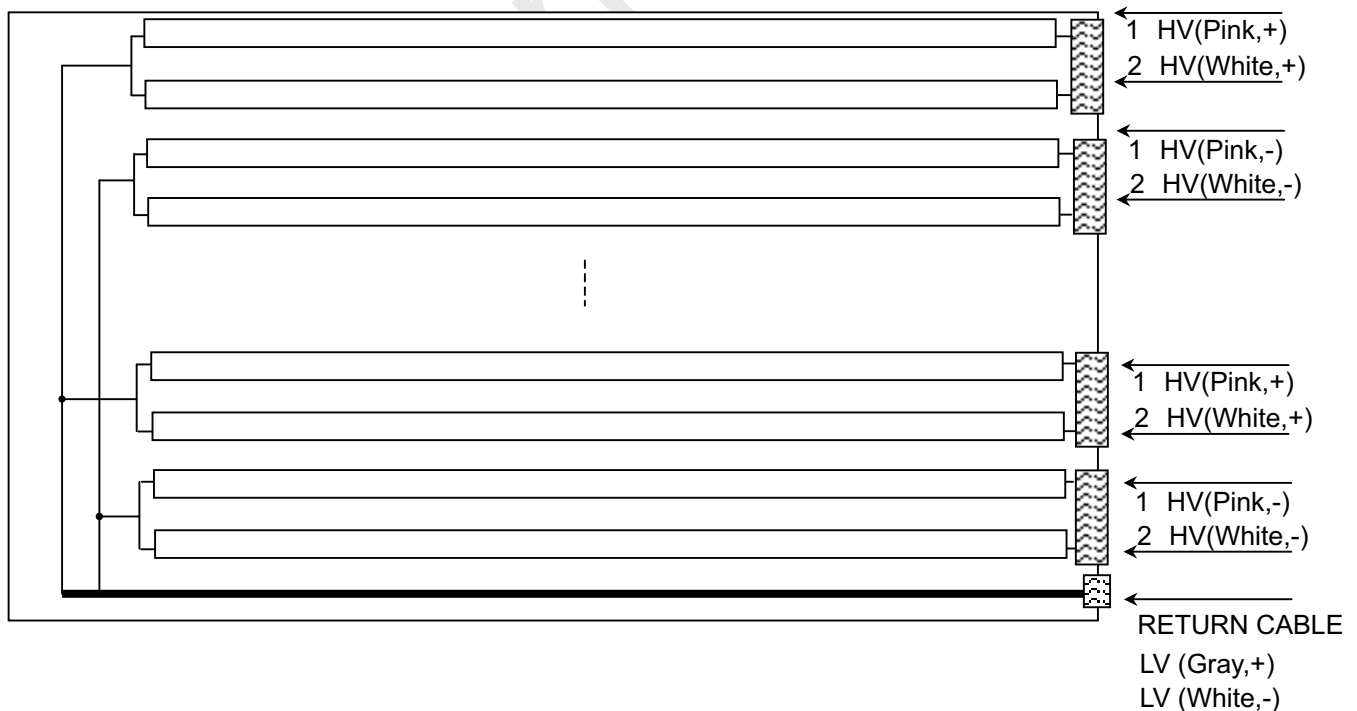
5. INTERFACE PIN CONNECTION

5.1 TFT LCD MODULE

Pin assignment

Pin	Name	Description	Pin	Name	Description
1	NC	No Connection	26	R0	Red Data
2	NC	No Connection	27	GND	Ground
3	NC	No Connection	28	G7	Green Data (G7:MSB)
4	GND	Ground	29	G6	
5	GND	Ground	30	G5	
6	VCC	Power Input (+5.0V)	31	G4	
7	VCC		32	GND	Ground
8	VCC		33	G3	Green Data
9	VCC		34	G2	
10	GND	Ground	35	G1	
11	NC		36	G0	
12	NC		37	GND	Ground
13	GND	Ground	38	B7	Blue Data (B7:MSB)
14	DE	Data Enable	39	B6	
15	GND	Ground	40	B5	
16	DCLK	Dot Clock	41	B4	
17	GND	Ground	42	GND	Ground
18	R7	Red Data (R7:MSB)	43	B3	Blue Data
19	R6		44	B2	
20	R5		45	B1	
21	R4		46	B0	
22	GND	Ground	47	GND	Ground
23	R3	Red Data	48	GND	Ground
24	R2		49	NC	No Connection
25	R1		50	NC	No Connection

Note (1) Connector Part No.: FH12-50S-0.5SH (Hirose) or compatible

Note (2) NC pin has to keep high impedance

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5.2 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																						
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	R7	R6	G5	G4	G3	G2	G1	G0	R7	R6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
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	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

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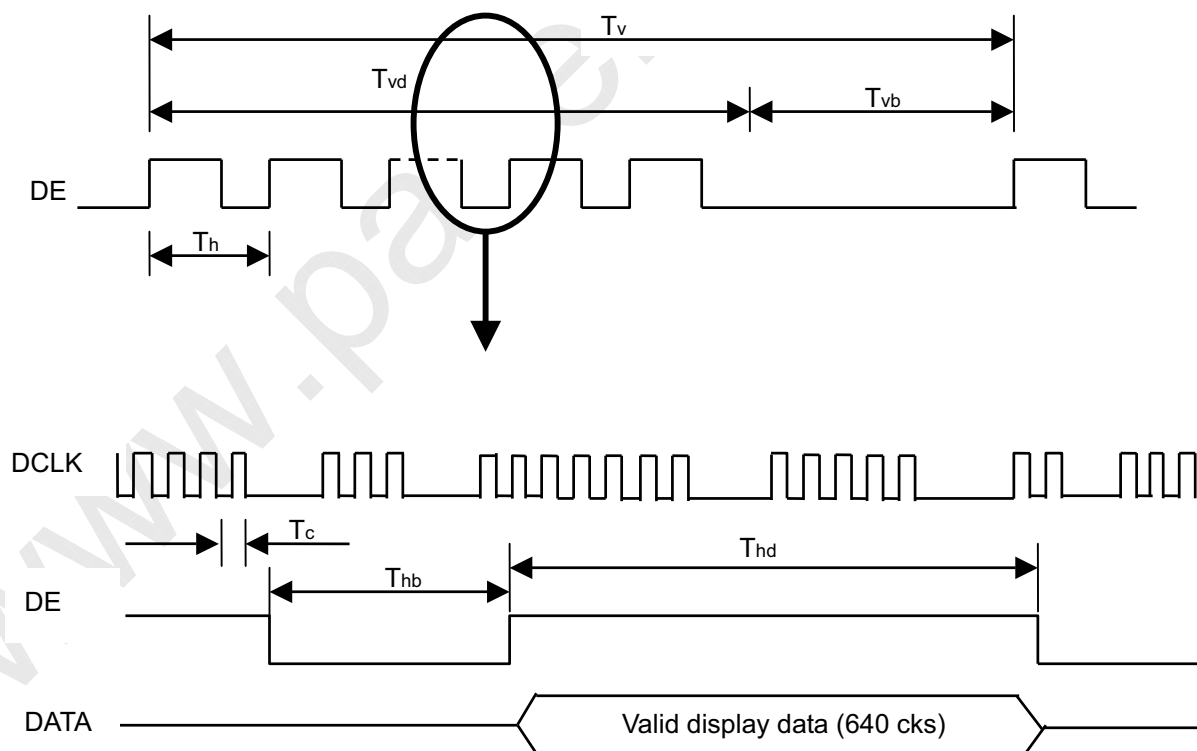
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6. INTERFACE TIMING

6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Clock	Frequency	1/Tc	(20)	25.175	(30)	MHz	-
Vertical Active Display Term	Frame Rate	Fr	(50)	60	(70)	Hz	-
	Total	Tv	(500)	525	(550)	Th	Tv=Tvd+Tvb
	Display	Tvd	480	480	480	Th	-
	Blank	Tvb	(20)	45	(70)	Th	-
Horizontal Active Display Term	Total	Th	(700)	800	(900)	Tc	Th=Thd+Thb
	Display	Thd	640	640	640	Tc	-
	Blank	Thb	(60)	160	(260)	Tc	-

Note: Because of this module is operated by DE only mode, Hsync and Vsync input signals should be set

to low logic level or ground. Otherwise, this module would operate abnormally.

INPUT SIGNAL TIMING DIAGRAM

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6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.

Power Supply Vcc

0.9 Vcc
0.1Vcc
0V

0.9 Vcc
0.1Vdd

$0 \leqq T_1 \leqq (10ms)$
$0 \leqq T_2 \leqq (50ms)$
$0 \leqq T_3 \leqq (50ms)$
$(500ms) \leqq T_4$

T_1
T_2

T_3
T_4

Signals

VALID

Power On

Power Off

Backlight (Recommended)
$(100)ms \leqq T_5$
$100ms \leqq T_6$

50%   50%

T_5 T_6

Power ON/OFF Sequence
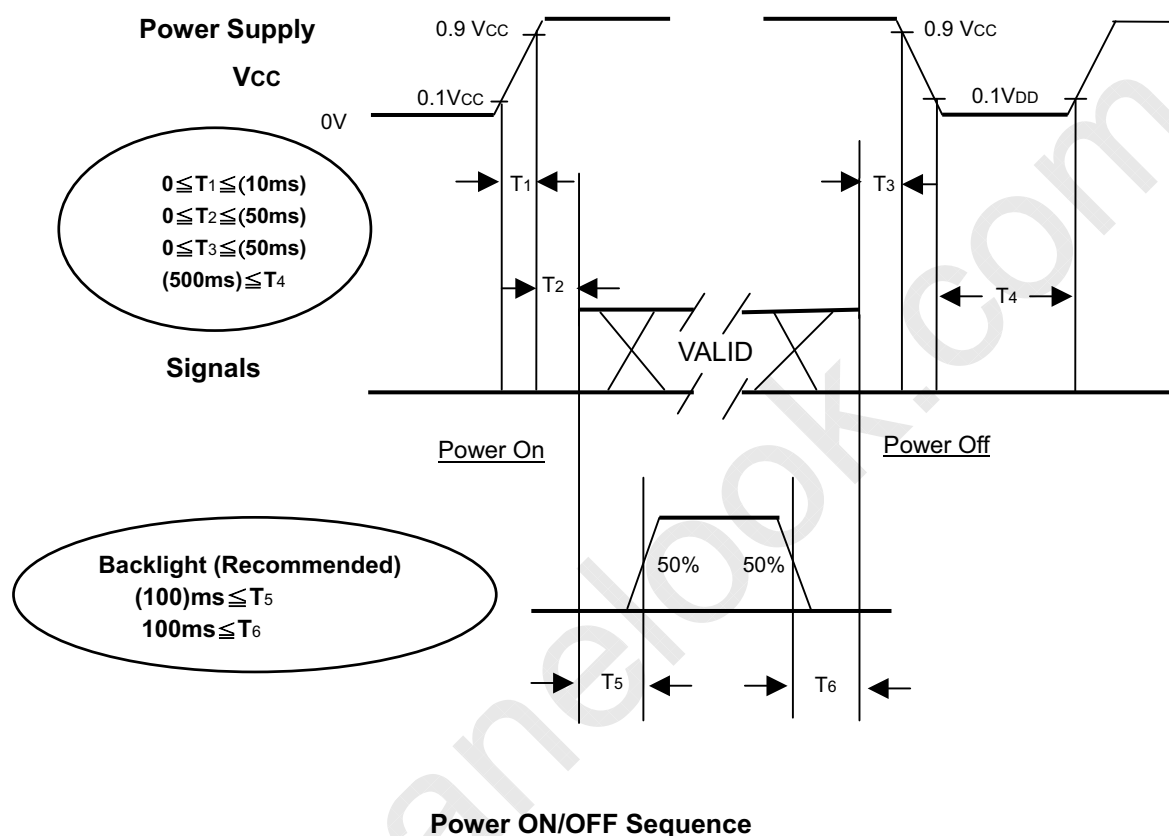
Note (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.

Note (2) Apply the lamp voltage within the LCD operation range. When the backlight turns on before the LCD operation or the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.

Note (3) In case of vcc = off level, please keep the level of input signals on the low or keep a high impedance.

Note (4) T4 should be measured after the module has been fully discharged between power off and on period.

Note (5) Interface signal shall not be kept at high impedance when the power is on.

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7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

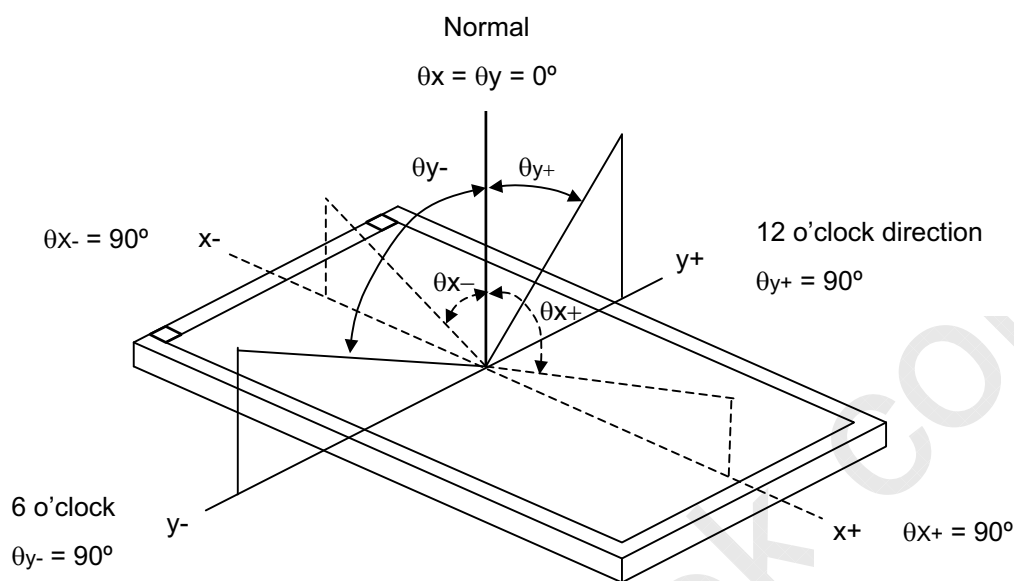| Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	^{o}C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V_{CC}	5.0	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
Inverter Current	I_L	(4.2)	mA
Inverter Driving Frequency	F_L	(33)	KHz
Inverter	(DELTA / DARFON)		

7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2. The following items should be

measured under the test conditions described in 7.1 and stable environment shown in Note (6).

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR		(450)	(600)	-	-	Note(2)
Response Time		T_R		-	(15)	(25)	ms	Note(3)
		T_F		-	(10)	(20)	ms	
		Gray to gray			(16.6)	(25)		Note(4)
Center Luminance of White		L_C		(400)	(500)	-	cd/m^2	Note(5)
White Variation		δW		-	-	(1.25)	-	Note(8)
Cross Talk		CT	θ_x=0°, θ_Y =0°	-	-	(4)	%	Note(6)
Color Chromaticity	Red	Rx	Viewing Normal Angle	(0.611)	(0.641)	(0.671)	-	Color Chromaticity
		Ry		(0.300)	(0.330)	(0.360)	-	
	Green	Gx		(0.236)	(0.266)	(0.296)	-	
		Gy		(0.568)	(0.598)	(0.628)	-	
	Blue	Bx		(0.114)	(0.144)	(0.174)	-	
		By		(0.038)	(0.068)	(0.098)	-	
	White	Wx		(0.242)	(0.272)	(0.302)	-	
		Wy		(0.248)	(0.278)	(0.308)	-	
	Color Gamut			(72)	(75)	-	%	
Viewing Angle	Horizontal	θ_x+	CR≥10	(80)	(85)	-	Deg.	Viewing Angle
		θ_x-		(80)	(85)	-		
	Vertical	θ_Y+		(80)	(85)	-		
		θ_Y-		(80)	(85)	-		

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Note (1) Definition of Viewing Angle (θx, θy):

Viewing angles are measured by Eldim EZ-Contrast 160R

Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.
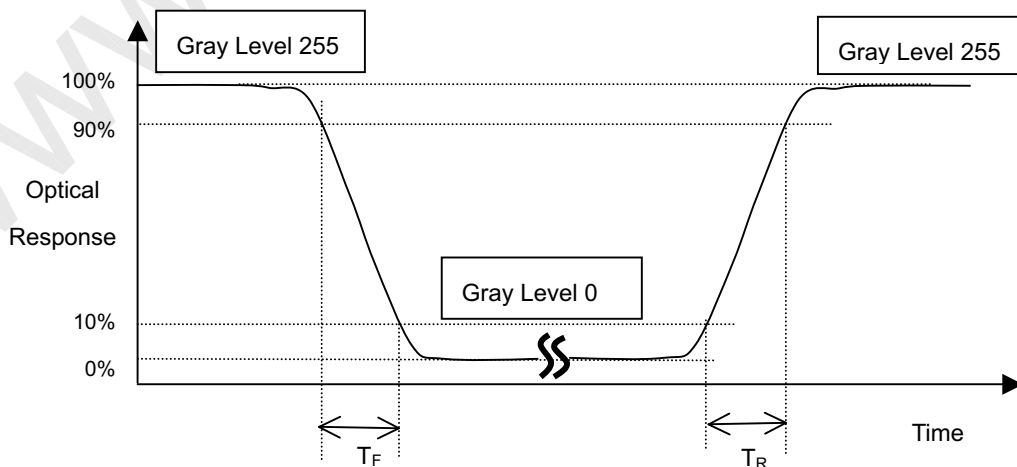
Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255

L 0: Luminance of gray level 0

CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at the figure in Note (7).

Note (3) Definition of Response Time (T_R, T_F):

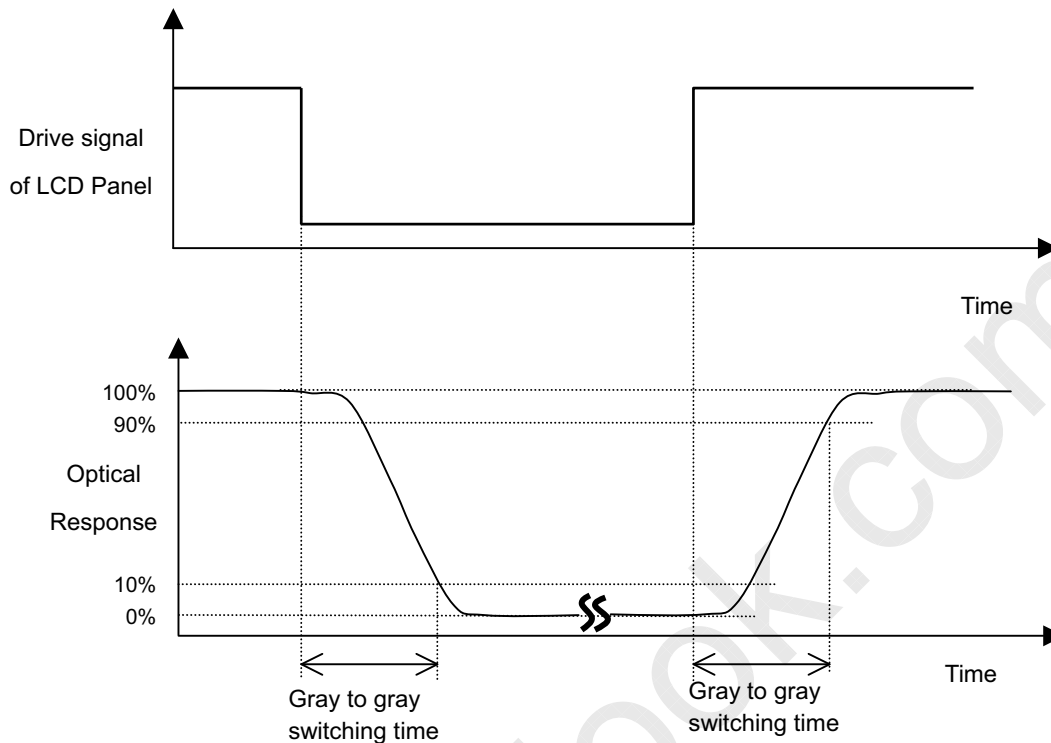16

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Note (4) Definition of Gray to Gray Switching Time:

Drive signal
of LCD Panel

Time

100%
90%

Optical

Response

10%
0%

Time

Gray to gray
switching time

Gray to gray
switching time

The driving signal means the signal of gray level 0, 63, 127, 191, 255.

Note (5) Definition of Luminance of White (L_C, L_{AVE}):

Measure the luminance of gray level 255 at center point and 5 points

$L_C = L (5)$

$L_{AVE} = [L (1)+ L (2)+ L (3)+ L (4)+ L (5)] / 5$

L (x) is corresponding to the luminance of the point X at the figure in Note (7).

Note (6) Definition of Cross Talk (CT):

$CT = | Y_B – Y_A | / Y_A \times 100 (\%)$

Where:

Y_A = Luminance of measured location without gray level 0 pattern (cd/m^2)

Y_B = Luminance of measured location with gray level 0 pattern (cd/m^2)

(0, 0)　　Active Area

$Y_{A, U}$ (D/2,W/8)

$Y_{A, L}$ (D/8,W/2)　　Gray 128　　$Y_{A, R}$ (7D/8,W/2)

$Y_{A, D}$ (D/2,7W/8)

(D,W)

(0, 0)　　Active Area

$Y_{B, U}$ (D/2,W/8)

(D/4,W/4)

$Y_{B, L}$ (D/8,W/2)　　$Y_{B, R}$ (7D/8,W/2)

(3D/4,3W/4)

$Y_{B, D}$ (D/2,7W/8)　　Gray 128

(D,W)

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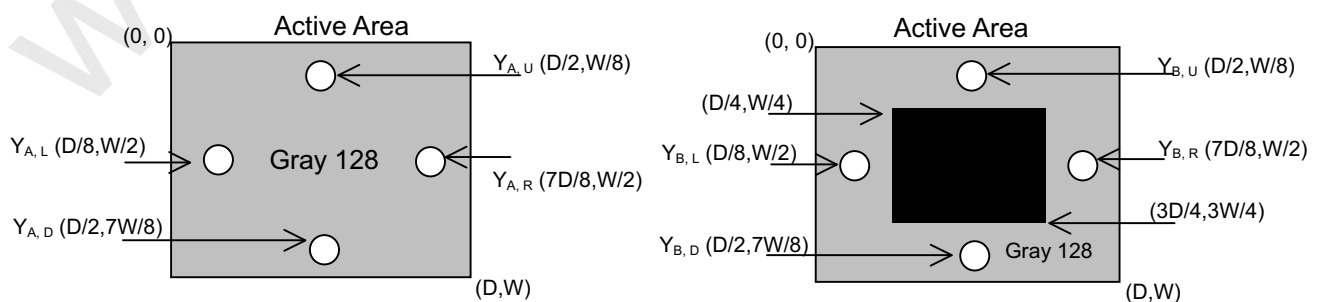
Note (7) Measurement Setup:

The LCD module should be stabilized at given temperature for 1 hour to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 1 hour in a windless room.
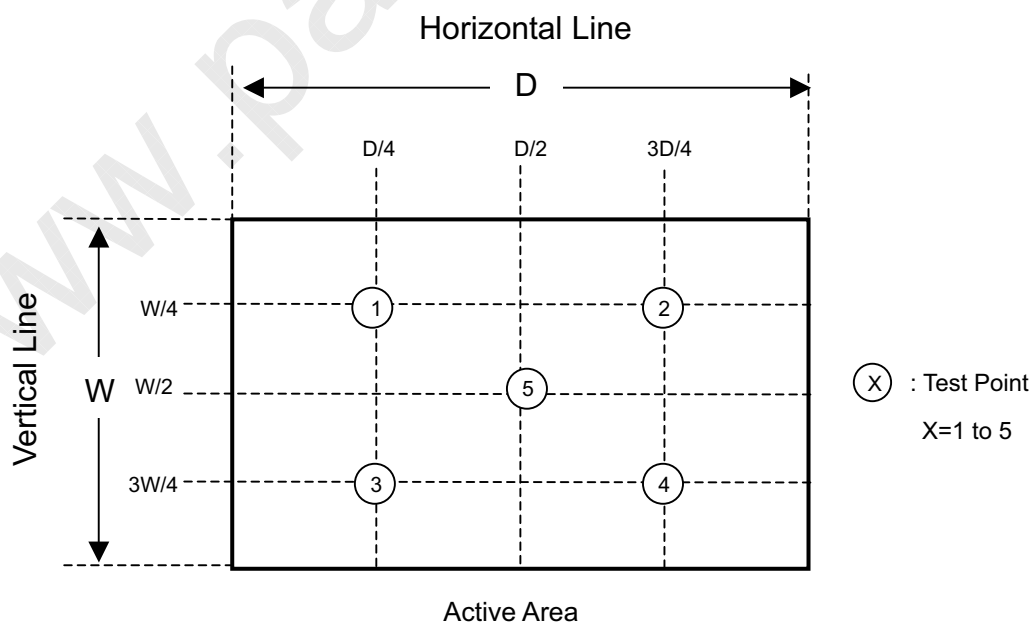
LCD Module

LCD Panel

Center of the Screen

Display Color Analyzer
(Minolta CA210)

Light Shield Room

(Ambient Luminance < 2 lux)
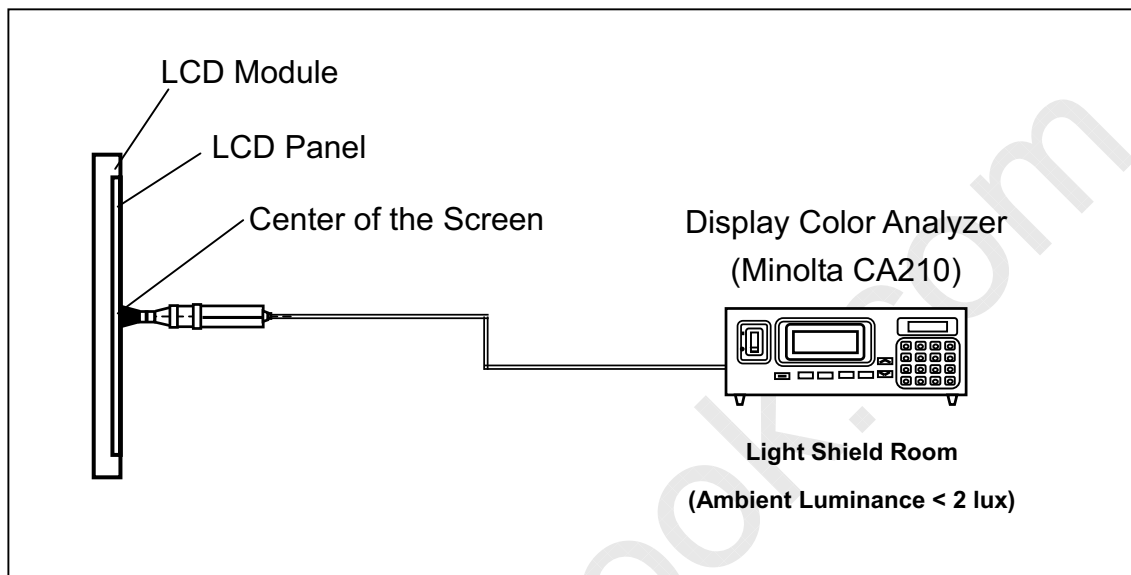
Note (8) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 5 points

δW = Maximum [L (1), L (2), L (3), L (4), L (5)] / Minimum [L (1), L (2), L (3), L (4), L (5)]

Horizontal Line

D

D/4   D/2   3D/4

Vertical Line

W/4 —— ①   ②

W   W/2 —— ⑤   Ⓧ : Test Point

X=1 to 5

3W/4 —— ③   ④

Active Area

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8. PACKAGING

8.1 PACKING SPECIFICATIONS

(1)   5 TV LCD modules / 1 Box

(2)   Box dimensions : 535(L) X 357 (W) X 460 (H)

(3)   Weight : approximately 13Kg (5 modules per box)

8.2 PACKING Method

Figures 9-1 and 9-2 are the packing method

Carton dimensions:535(L)x357(W)x460(H)mm
Weight : Approx. 13Kg(5modules per 1 carton)
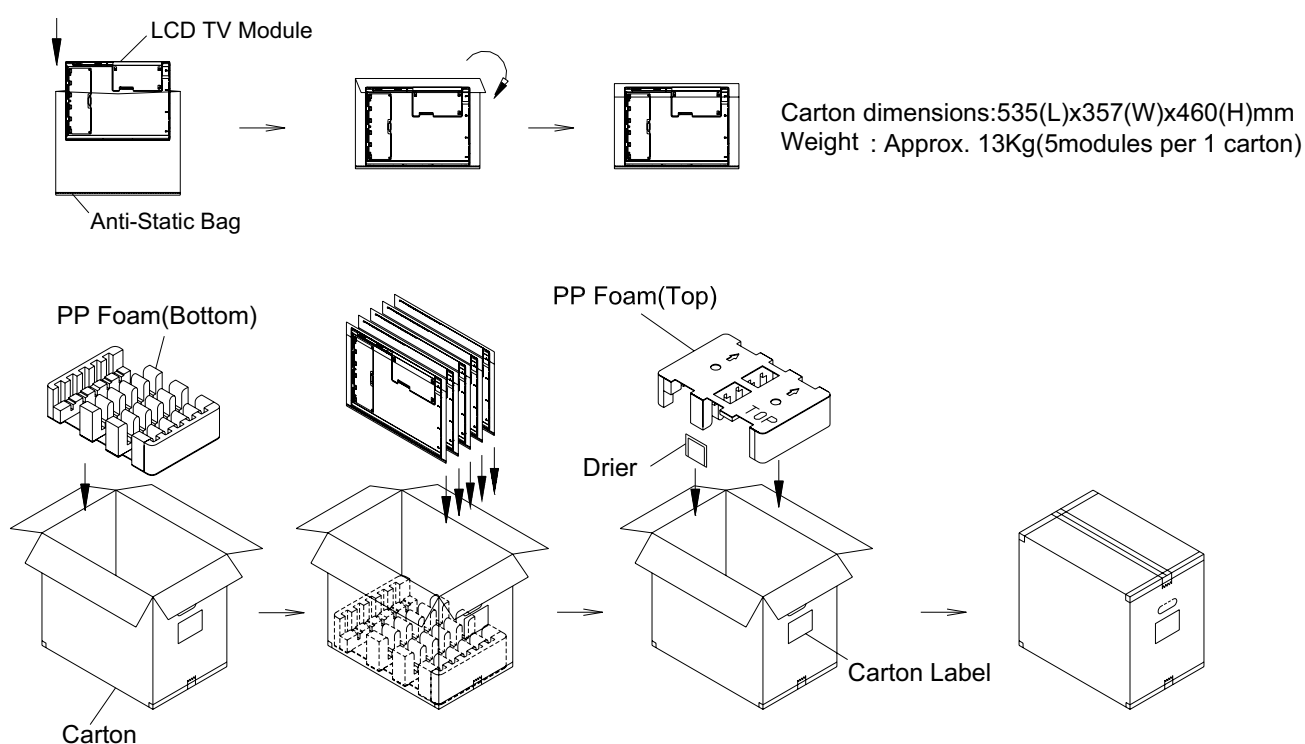
Figure.9-1 packing method

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Corner Protector:L1350*50mm*50mm

Pallet:L1100*W1100*H135mm

Bottom Cap:L1100*W1100*H120mm

Pallet Stack:L1100*W1100*H1515mm
Gross:260kg

PE Sheet

Corner Protector

Carton Label

Film

PP Belt

Bottom Cap

Pallet

Figure. 9-2 Packing method

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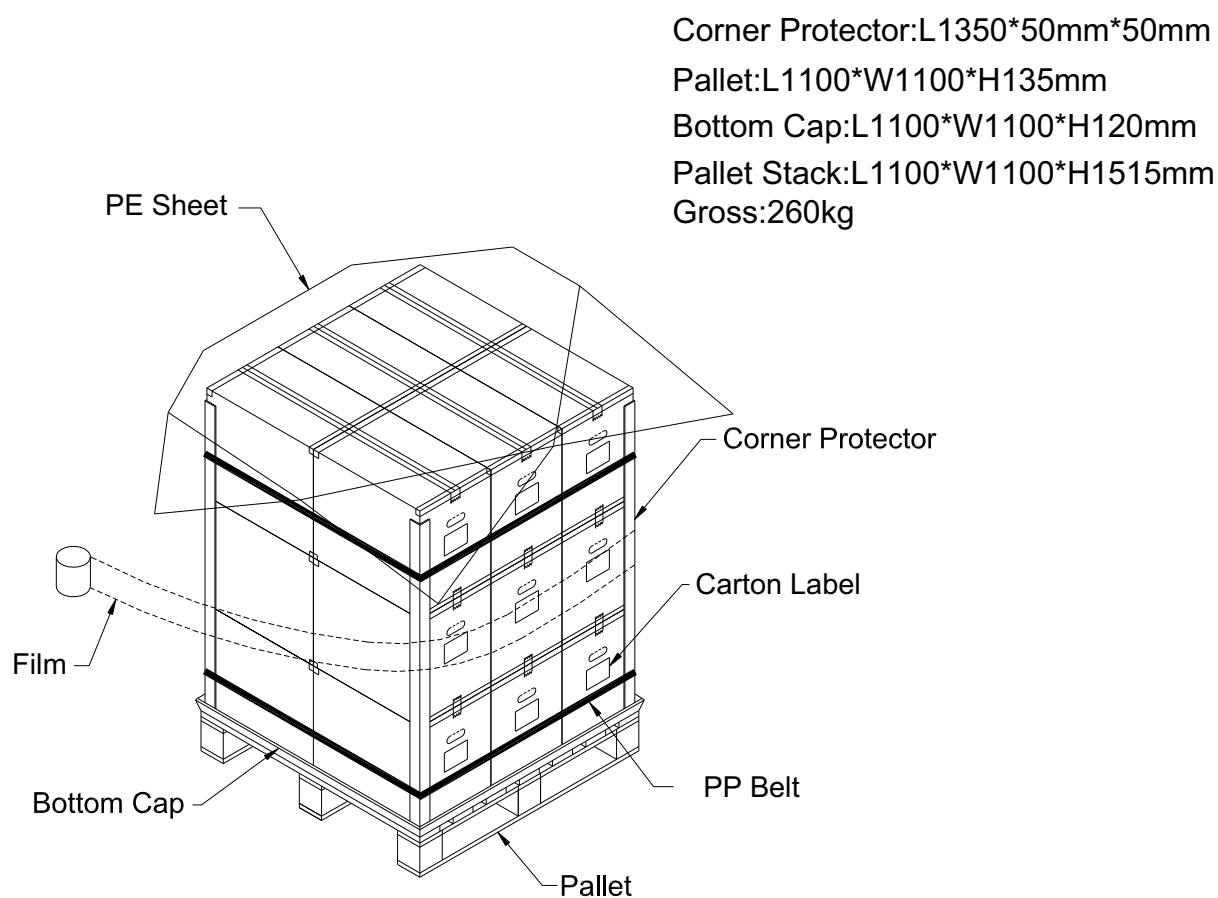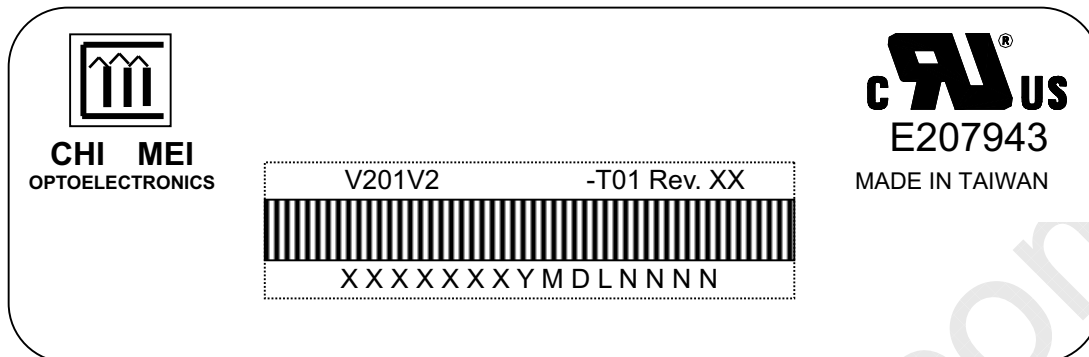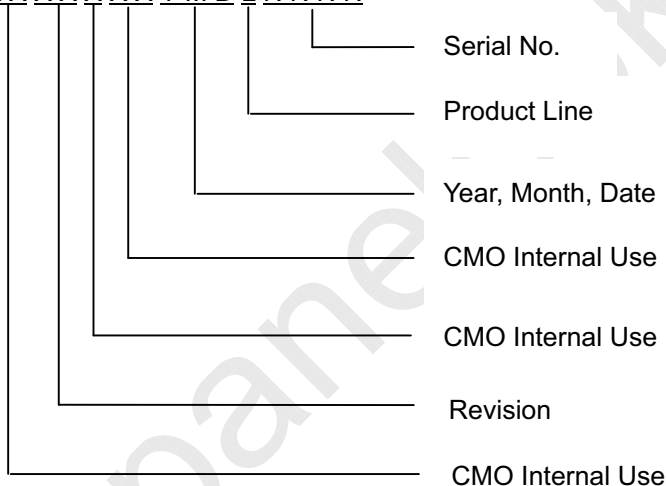
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9. DEFINITION OF LABELS

9.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.

```
   CHI  MEI          V201V2          -T01 Rev. XX      E207943
   OPTOELECTRONICS                                     MADE IN TAIWAN
                     ‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖‖
                       X X X X X X X Y M D L N N N N
```

(a) Model Name: V201V2-T01

(b) Revision: Rev. XX, for example: A0, A1… B1, B2… or C1, C2…etc.

(c) Serial ID: X X X X X X X Y M D L N N N N

```
                                              Serial No.

                                              Product Line

                                              Year, Month, Date

                                              CMO Internal Use

                                              CMO Internal Use

                                              Revision

                                              CMO Internal Use
```

Serial ID includes the information as below:

   (a) Manufactured Date: Year: 1~9, for 2000~2009

   Month: 1~9, A~C, for Jan. ~ Dec.

   Day: 1~9, A~Y, for 1st to 31st, exclude I ,O, and U.

   (b) Revision Code: Cover all the change

   (c) Serial No.: Manufacturing sequence of product

   (d) Product Line: 1 -> Line1, 2 -> Line 2, …etc.

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10. PRECAUTIONS

10.1 ASSEMBLY AND HANDLING PRECAUTIONS

(1) Do not apply rough force such as bending or twisting to the module during assembly.

(2) It is recommended to assemble or to install a module into the user's system in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.

(3) Do not apply pressure or impulse to the module to prevent the damage of LCD panel and Backlight.

(4) Always follow the correct power-on sequence when the LCD module is turned on. This can prevent the damage and latch-up of the CMOS LSI chips.

(5) Do not plug in or pull out the I/F connector while the module is in operation.

(6) Do not disassemble the module.

(7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.

(8) Moisture can easily penetrate into LCD module and may cause the damage during operation.

(9) High temperature or humidity may deteriorate the performance of LCD module. Please store LCD modules in the specified storage conditions.

(10) When ambient temperature is lower than 10ºC, the display quality might be reduced. For example, the response time will become slow, and the starting voltage of CCFL will be higher than that of room temperature.

10.2 SAFETY PRECAUTIONS

(1) The startup voltage of a Backlight is approximately 1000 Volts. It may cause an electrical shock while assembling with the inverter. Do not disassemble the module or insert anything into the Backlight unit.

(2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.

(3) After the module's end of life, it is not harmful in case of normal operation and storage.

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11.MECHANICAL CHARACTERISTICS

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